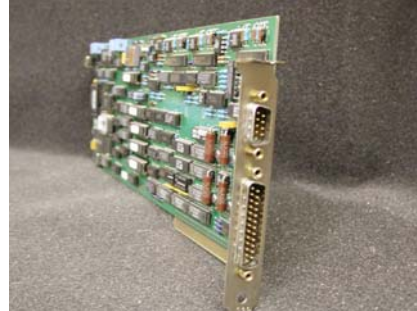


CVSD Voice Decoder (ISA) CVSD-300

Features

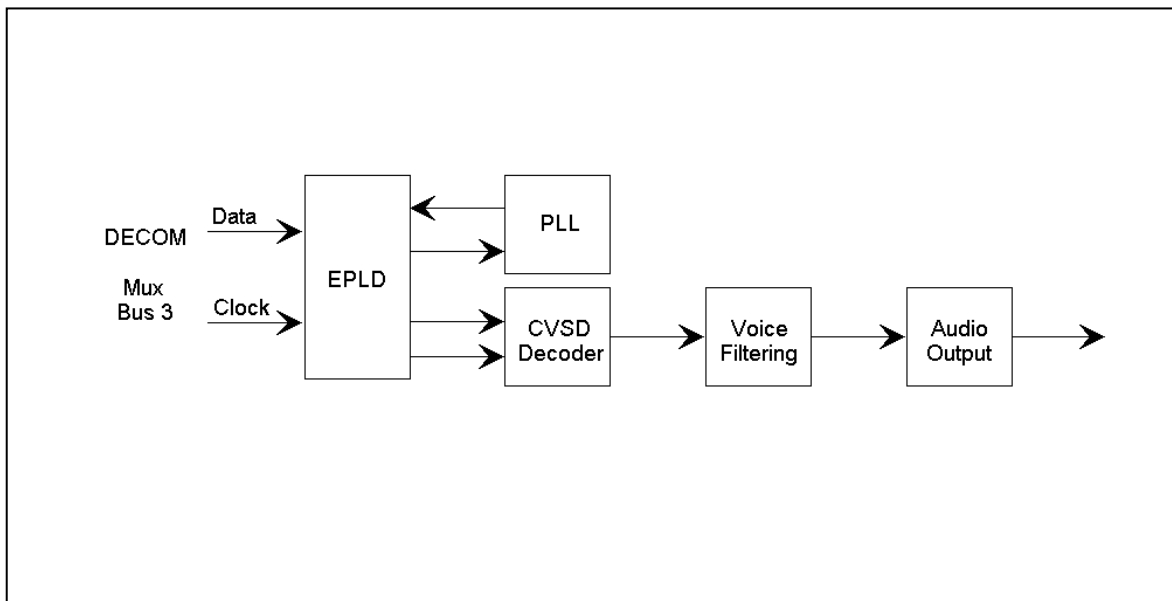
- CVSD Demodulator
- 10 to 30K Samples/Second
- ± 10 Volt Output
- Auto Data Size Detection



Introduction

The voice reconstructor is designed to input CVSD-coded data and to re-create the audio signal for output to an audio amplifier with a 600-ohm input impedance. The board is designed to interface with a PCM Decom. There are no VME interface signals used, and no jumper selections are necessary for board operation. The CVSD data in the PCM stream is output from the Decom as embedded data on channel three. The CVSD board inputs the channel three embedded data and clock.

The board counts the number of bits per PCM word and reconstructs a continuous clock using a phase-locked loop. The data is clocked into an onboard CVSD demodulator and filtered for output. It can process 8, 10, 12, 14, or 16-bit data words with a 10-30 kHz reconstructed voice bit rate.



Inputs	Source	One (DECOM MUX channel 3)
	Connector	DB15 male
	Data Level	TTL 0.8V - 2.0V
	Data Impedance	180-ohm
	Clock Level	TTL 0.8V - 2.0V
	Clock Impedance	180-ohm
	Clock Frequency	500 kHz - 5 MHz
Processing	Input Format	CVSD burst data from a Decom
	Clock	Gated 180° clock
	Data	CVSD data: 8, 10, 12, 14, and 16 size words
	CVSD Clock	VCO controlled
	CVSD Rate	10 kHz - 30 kHz
Output	Channels	One (1)
	Connector	DB9 female
	Level	Up to +/- 10V @ Rload => 500 ohms
Physical Description	Board Size	Single-slot, ISA
	Board Number	Single
	Operating Environment	Standard commercial
Electrical and Environmental	Power Requirements	+5 VDC @ 500 mA
		+12 VDC @ 100 mA
		-12 VDC @ 100 mA
	Operating Temperature	Operating 0° to 50 °C
Storage Temperature	-20 C to 80 °C	
Operating Humidity	Operating 20% to 95%, noncondensing	
Ordering Codes	CVSD-300-xxx	CVSDM Voice Decoder, ISA